

## **UHT Version 2.1 Release Notes**

With some USB 1.1 Hubs, Over Current Test was failing due to the sequence in which the bits were set and observed. In the previous revision the time delay between "Over Current Draw" and the bit test was set at 5 msec, which caused the failure since some Hubs require more time. In this revision this time delay is increased to 30 msec.